L Number	Hits	Search Text	DB	Time stamp
-	362747		USPAT;	2002/08/16 13:57
			US-PGPUB;	
			EPO; JPO;	
			DERWENT; IBM TDB	
_	10	((Integrated adj Circuit) and (PLD or	USPAT;	2002/08/16 14:09
-	10	"Programmable Logic")) and configur\$4 near	US-PGPUB;	2002/00/10 14.09
		(database or "data base" DBMS OODB DB)	EPO; JPO;	
		(DERWENT;	
			IBM_TDB	
-	67770	(word or bit) near line	USPAT;	2002/08/19 10:37
			US-PGPUB;	
			EPO; JPO; DERWENT;	
			IBM TDB	
i	5723	(((word or bit) near line) and cell) and	USPAT;	2002/08/19 10:38
		instance	US-PGPUB;	
			EPO; JPO;	·
			DERWENT;	
	200		IBM_TDB	0000/00/10 10 11
-	300	(((word or bit) near line) and cell) and (logical same (name id))	USPAT; US-PGPUB;	2002/08/19 10:41
		(1091Cal same (name 10))	EPO; JPO;	
			DERWENT;	
.			IBM TDB	
-	51	(((word or bit) near line) and cell) and	USPĀT;	2002/08/19 14:41
		(((((word or bit) near line) and cell) and	US-PGPUB;	
		instance) and (instance same path)) and	EPO; JPO;	
		((((word or bit) near line) and cell) and	DERWENT;	
	249	(logical same (name id))) cadence and hierarch\$3	IBM_TDB USPĀT;	2002/08/19 14:42
	243	cadence and metatemys	US-PGPUB;	2002/00/13 14.42
			EPO; JPO;	
		,	DERWENT;	
			IBM_TDB	
j -	26	707/100 and (Logic adj device)	USPAT;	2002/08/19 15:15
			US-PGPUB; EPO; JPO;	
			DERWENT;	
			IBM TDB	
_	46	(· · · · · · · · · · · · · · · · · · ·	USPĀT;	2002/08/19 15:30
		(327/37-39.ccls. and address)	US-PGPUB;	
			EPO; JPO;	
			DERWENT;	
	15	(CADENCE.AS. and config\$5) and address	IBM_TDB USPAT;	2002/08/19 15:35
	13	, siles and confrages, and address	US-PGPUB;	2002/00/19 13:33
			EPO; JPO;	
			DERWENT;	
			IBM_TDB	
-	578	((((memory near cell) and (PLD or logic))	USPAT;	2002/08/19 16:05
		<pre>and (bit with order\$4)) and configur\$5) and (identify same address)</pre>	US-PGPUB;	
		and (Idencity same address)	EPO; JPO; DERWENT;	
			IBM TDB	
-	19	(((((memory near cell) and (PLD or logic))	USPAT;	2002/08/19 16:19
		and (bit with order\$4)) and configur\$5)	US-PGPUB;	
		and (identify same address)) and (load\$4	EPO; JPO;	
		near device)	DERWENT;	
_	216	FPGA and (bit near order)	<pre>IBM_TDB USPAT;</pre>	2002/00/10 16-50
	210	Tron and (bit hear order)	US-PGPUB;	2002/08/19 16:50
			EPO; JPO;	
ľ			DERWENT;	
			IBM_TDB	
-	162	(FPGA and (bit near order)) and configur\$4	USPĀT;	2002/08/20 07:27
			US-PGPUB;	
			EPO; JPO;	
			DERWENT; IBM TDB	
			TD13 1 DD	l

					
-	97	((((((configuration same block) and	USPAT;	2002/08/20 0	8:22
		schema\$4) and programmable same (device	US-PGPUB;		
		array)) and hierarch\$4) and address) and	EPO; JPO;		
		(memory same cell)) and simulat\$4	DERWENT;		
			IBM_TDB		
_	1032	((stor\$4 load\$4) and order and configur\$4)	USPAT;	2002/08/27 1	0:55
		and ("logical unit")	US-PGPUB;		
			EPO; JPO;		
			DERWENT;		
			IBM_TDB		
_	62	, (,	USPAT;	2002/08/27 1	1:08
		same (programmable CPLD PLD FPGA Array)	US-PGPUB;		
			EPO; JPO;		
			DERWENT;		
			IBM_TDB		
_	46	(loading with (programm\$4 near6 (device	USPAT;	2002/09/03 0	9:26
		array) PLD) same (order sequence)) and	US-PGPUB;		
		address	EPO; JPO;		
			DERWENT;		
	1.50		IBM_TDB		
-	163		USPAT;	2002/09/03 1	6:27
		and location) and (X Y)) and bitline) and	US-PGPUB;		
		wordline same (X Y))	EPO; JPO;		
			DERWENT;		
			IBM_TDB		
-	21	(((""""" """" """"" """"" """"" """"" """"""	USPAT;	2002/09/03 1	6:28
		bitline) and wordline near (X Y)	US-PGPUB;		
			EPO; JPO;		
			DERWENT;		
	1		I IBM TDB	i	